

00237-2234200

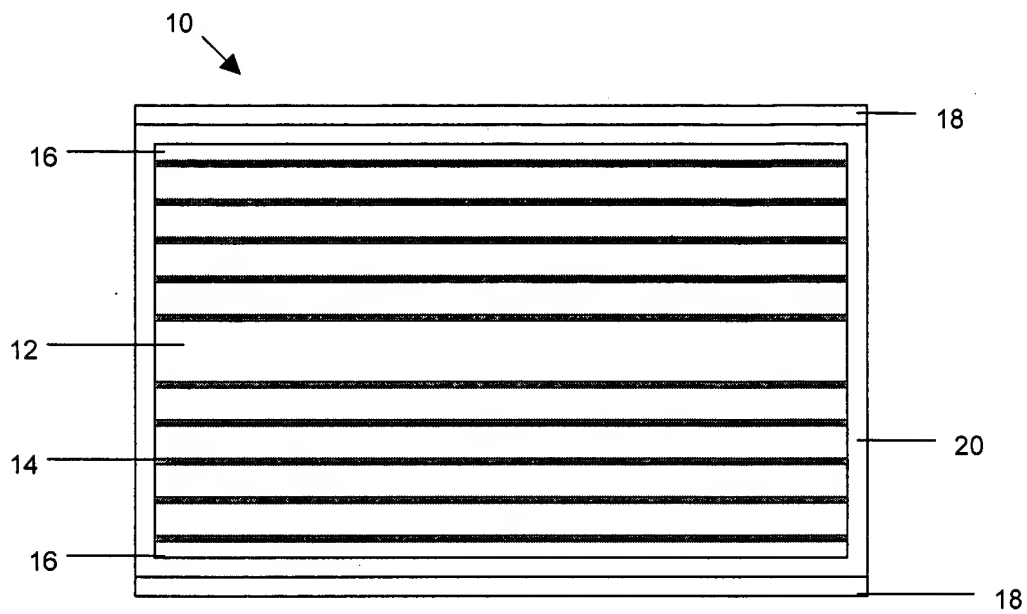


FIG. 1

FIG. 2

The diagram illustrates a pixel driver circuit for two pixels, 30 and 40. Each pixel has a pixel drive circuitry block (32, 42) and a bypass bit latch block (34, 44). The drive circuitry outputs to a tri-state buffer (38, 48). The bypass bit latch outputs to the same tri-state buffer. The tri-state buffer outputs to a resistor (R 50) which is connected to the pixel (30, 40).

FIG. 3

VIA CUT BY LASER TO DISCONNECT PIXEL FROM DEFECTIVE DRIVE CIRCUIT

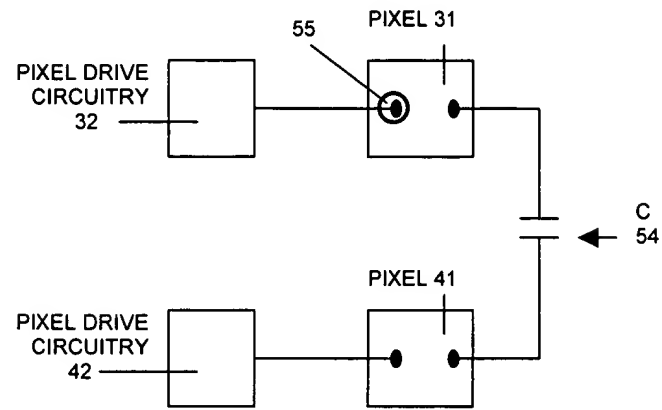


FIG. 5

002222T-2293h/60

VIA CUT BY LASER TO DISCONNECT PIXEL FROM DEFECTIVE DRIVE CIRCUIT

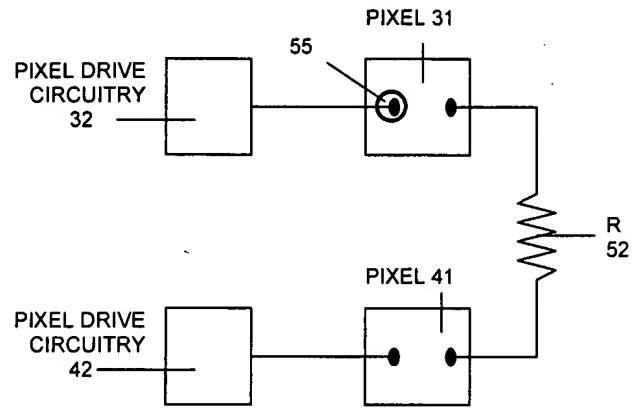
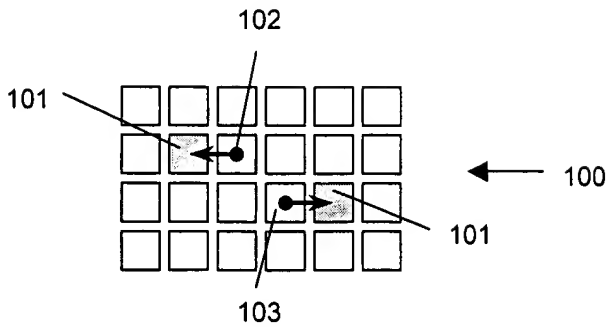


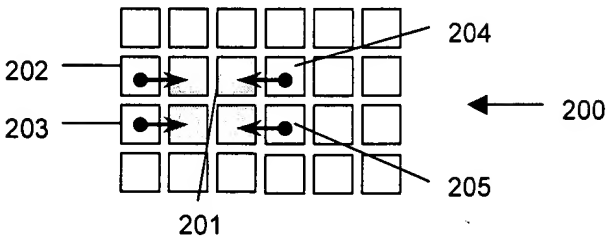
FIG. 4

00000000-00000000

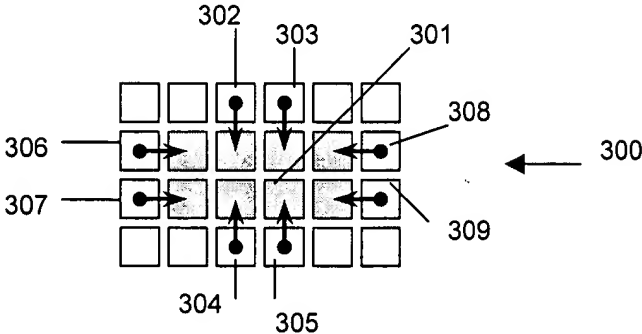
SINGLE-PIXEL (1 X 1).



4-PIXEL GROUP (2 X 2).



8-PIXEL GROUP (4 X 2).



16-PIXEL GROUP (4 X 4).

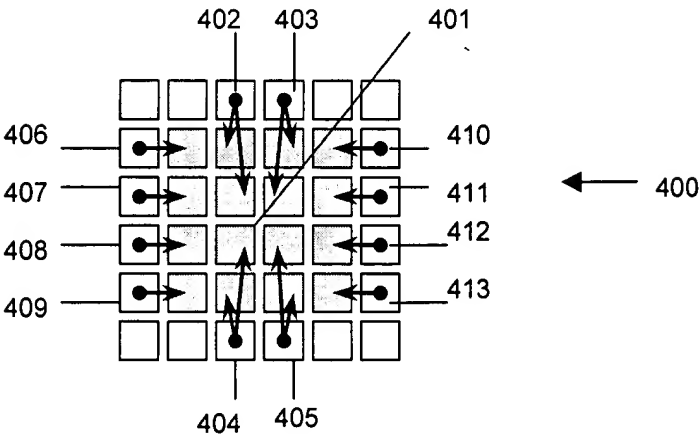


FIG. 6